

## FDS6875

# **Dual P-Channel 2.5V Specified PowerTrench™ MOSFET**

## **General Description**

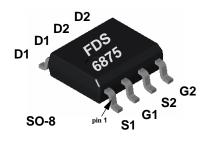
These P-Channel 2.5V specified MOSFETs are produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

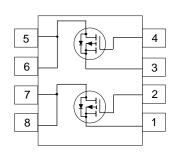
These devices are well suited for portable electronics applications: load switching and power management, battery charging and protection circuits.

## **Features**

- Low gate charge (23nC typical).
- High performance trench technology for extremely low R<sub>DS/OND</sub>.
- High power and current handling capability.







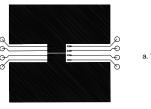
# **Absolute Maximum Ratings** $T_A = 25^{\circ}C$ unless otherwise noted

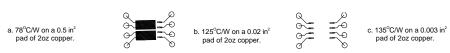
Symbol	Parameter	FDS6875	Units
V <sub>DSS</sub>	Drain-Source Voltage	-20	V
V <sub>GSS</sub>	Gate-Source Voltage	±8	V
D	Drain Current - Continuous (Note 1a)	-6	А
	- Pulsed	-20	
$P_{\scriptscriptstyle D}$	Power Dissipation for Dual Operation	2	W
	Power Dissipation for Single Operation (Note 1a)	1.6	
	(Note 1b)	1	
	(Note 1c)	0.9	
$T_{J}$ , $T_{STG}$	Operating and Storage Temperature Range	-55 to 150	°C
THERMA	L CHARACTERISTICS		
R <sub>eja</sub>	Thermal Resistance, Junction-to-Ambient (Note 1a)	78	°C/W
R <sub>eJC</sub>	Thermal Resistance, Junction-to-Case (Note 1)	40	°C/W

Symbol	Parameter	Conditions	Min	Тур	Max	Units	
OFF CHAR	ACTERISTICS						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$		-20			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient	I <sub>D</sub> = -250 μA, Referenced	to 25 °C		-21		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, \ V_{GS} = 0 \text{ V}$				-1	μA
			$T_J = 55^{\circ}C$			-10	μΑ
GSSF	Gate - Body Leakage, Forward	$V_{GS} = 8 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
I <sub>GSSR</sub>	Gate - Body Leakage, Reverse	$V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
ON CHARA	CTERISTICS (Note 2)			ı	I	I	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		-0.4	-0.8	-1.5	V
$\Delta V_{GS(th)}/\Delta T_{J}$	Gate Threshold Voltage Temp. Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to	o 25 °C		2.8		mV/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS} = -4.5 \text{ V}, I_{D} = -6 \text{ A}$			0.024	0.03	Ω
, ,			T <sub>J</sub> =125°C		0.033	0.048	
		$V_{GS} = -2.5 \text{ V}, I_{D} = -5.3 \text{ A}$			0.032	0.04	
I <sub>D(ON)</sub>	On-State Drain Current	$V_{GS} = -4.5 \text{ V}, \ V_{DS} = -5 \text{ V}$		-20			Α
g <sub>FS</sub>	Forward Transconductance	$V_{DS} = -4.5 \text{ V}, I_{D} = -6 \text{ A}$			22		S
DYNAMIC (	CHARACTERISTICS	•		•	•		
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -10 \text{ V}, \ V_{GS} = 0 \text{ V},$	$V_{DS} = -10 \text{ V}, \ V_{GS} = 0 \text{ V},$				pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		500		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			200		pF	
SWITCHING	CHARACTERISTICS (Note 2)						
t <sub>D(on)</sub>	Turn - On Delay Time	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ A}$			8	16	ns
t,	Turn - On Rise Time	$V_{GEN} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$			15	27	ns
t <sub>D(off)</sub>	Turn - Off Delay Time				98	135	ns
t,	Turn - Off Fall Time				35	55	ns
$Q_g$	Total Gate Charge	$V_{DS} = -10 \text{ V}, I_{D} = -6 \text{ A},$			23	31	nC
$Q_{gs}$	Gate-Source Charge	V <sub>GS</sub> = -5 V			3.9		nC
$Q_{gd}$	Gate-Drain Charge			5.5		nC	
DRAIN-SOU	RCE DIODE CHARACTERISTICS AND MAX	(IMUM RATINGS					
I <sub>s</sub>	Maximum Continuous Drain-Source Diode Fo	orward Current				-1.3	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = -1.3 \text{ A}$ (Not	e 2)		-0.7	-1.2	V

#### Notes:

<sup>1.</sup>  $R_{g,u}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{g,c}$  is guaranteed by design while  $R_{gCA}$  is determined by the user's board design.



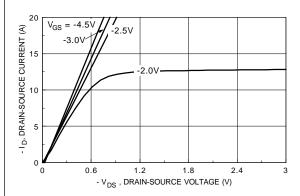




Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2.0%.

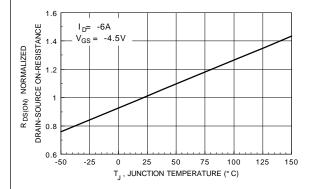
# **Typical Electrical Characteristics**



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Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Dain Current and Gate Voltage.



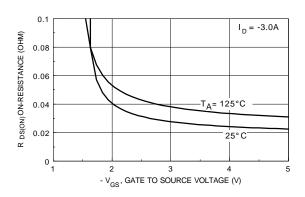
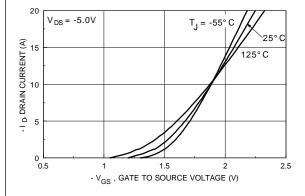


Figure 3. On-Resistance Variation with Temperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



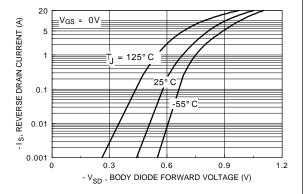
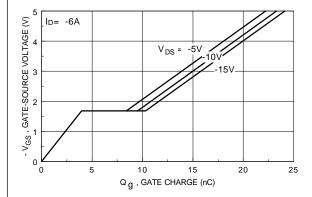


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

# **Typical Electrical Characteristics** (continued)



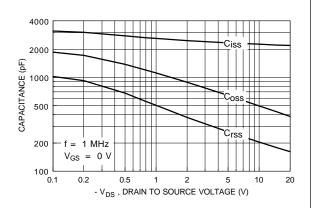
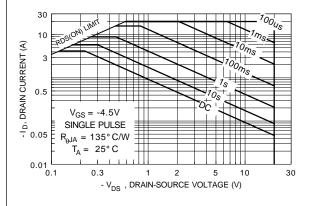


Figure 7. Gate Charge Characteristics.





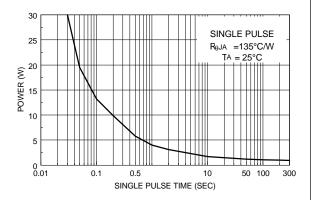


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

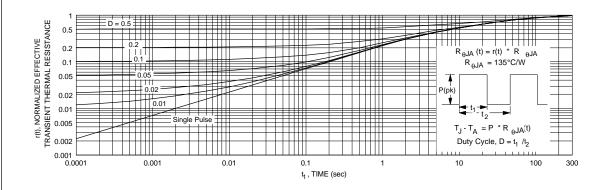
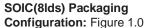


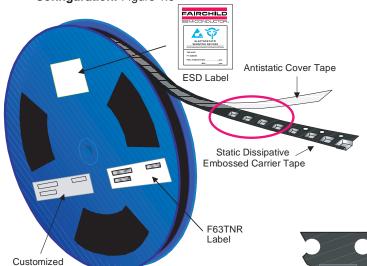
Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

# SO-8 Tape and Reel Data and Package Dimensions





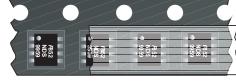


#### **Packaging Description:**

Packaging Description:

SOIC-8 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and amit-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13° or 300cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (antistatic coated). Other option comes in 500 units per 7° or 177cm diameter reel. This and some other options are further described in the Packaging Information table.

These full reles are individually barcode labeled and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains two reels maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped.





Packaging Option Standard o flow code) L86Z D84Z Rail/Tube TNR Packaging type TNR TNR Qty per Reel/Tube/Bag 2.500 4.000 500 13" Dia 13" Dia 7" Dia 343y64y343 530x130x83 343y64y343 184v187v47 5,000 30,000 8,000 1,000

Box Dimension (mm) Max qty per Box Weight per unit (gm) 0.0774 0.0774 0.0774 0.0774 Weight per Reel (kg) 0.6060 0.9696 0.1182 Note/Comments

SOIC (8lds) Packaging Information

# **SOIC-8 Unit Orientation**

### F63TNR Label sample

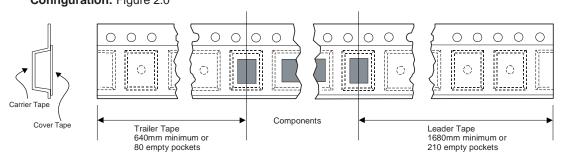
Label

Reel Size



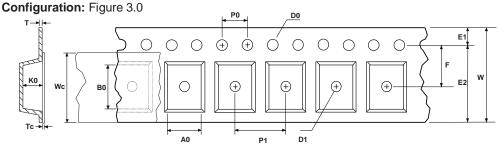
# 343mm x 342mm x 64mm Standard Intermediate box ESD Label F63TNL F63TN Label

## SOIC(8lds) Tape Leader and Trailer Configuration: Figure 2.0





# SOIC(8lds) Embossed Carrier Tape



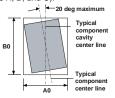


Dimensions are in millimeter														
Pkg type	A0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SOIC(8lds) (12mm)	6.50 +/-0.10	5.30 +/-0.10	12.0 +/-0.3	1.55 +/-0.05	1.60 +/-0.10	1.75 +/-0.10	10.25 min	5.50 +/-0.05	8.0 +/-0.1	4.0 +/-0.1	2.1 +/-0.10	0.450 +/- 0.150	9.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



Sketch B (Top View)

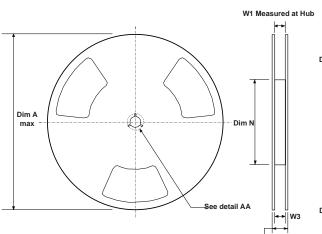
Component Rotation



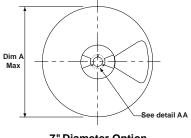
Sketch C (Top View)

Component lateral movement

# SOIC(8lds) Reel Configuration: Figure 4.0



13" Diameter Option



7" Diameter Option

B Min

Dim C

Dim D

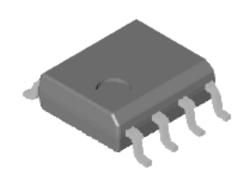
min

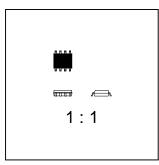
Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
12mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4
12mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	7.00 178	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4

W2 max Measured at Hub

# SO-8 Tape and Reel Data and Package Dimensions, continued

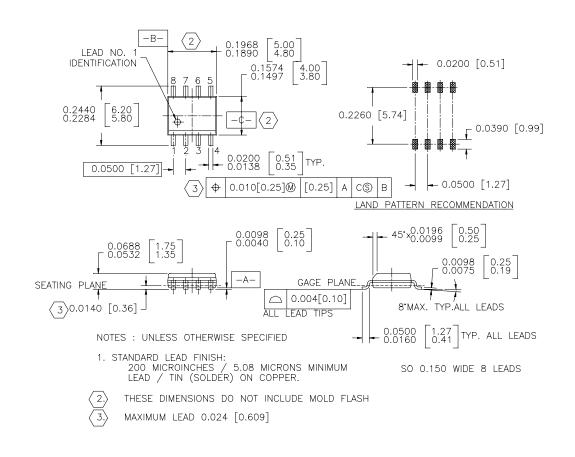
# SOIC-8 (FS PKG Code S1)





Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0774



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 $E^2CMOS^{TM}$  PowerTrench<sup>TM</sup>

FACT™ QFET™ FACT Quiet Series™ QS™

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Datasheet Identification	Product Status	Definition
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